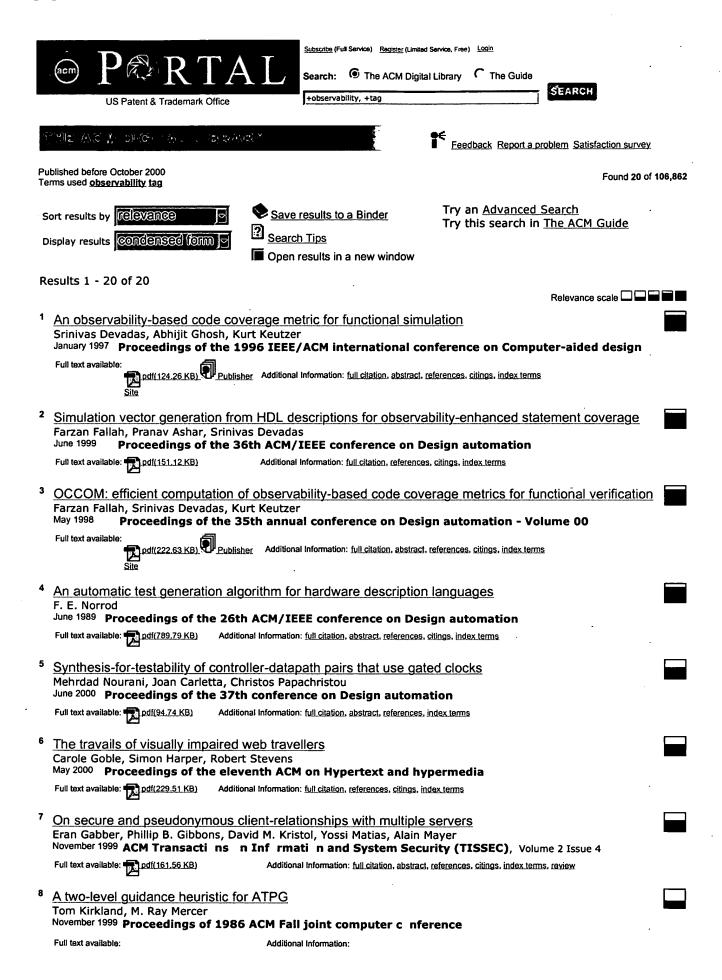
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13		h, David J. Farber Systems Review, Proceedings of the seventeenth ACM g systems principles, Volume 33 Issue 5 Additional Information: full citation, abstract, references, citings, index terms			
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15	Mark Horowitz, Margaret Martonosi, To May 1996 ACM SIGARCH Compute	ding memory performance feedback in modern processors dd C. Mowry, Michael D. Smith r Architecture News, Proceedings of the 23rd annual n on Computer architecture, Volume 24 Issue 2 Additional Information: full citation, abstract, references, citings, index terms			
16		sign Automation of Electronic Systems (TODAES), Volume 1 Issue 1 Additional Information: full citation, abstract, references, citings, index terms			
47					
"	Yutaka Miyabe, Csabe Biegl, Kazuhiko i June 1988 Proceedings of the first	gent supervisory system for a hot strip mill finisher Kawamura international conference on Industrial and engineering intelligence and expert systems - Volume 1 Additional Information: full citation, abstract, references, index terms			
	Karen L. Karavanic, Barton P. Miller January 1999 Proceedings of the 1999	ACM/IEEE conference on Supercomputing (CDROM) itional Information: full citation, references, citings, index terms			
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²⁰ Informing memory operations: memory performance feedback mechanisms and their applications Mark Horowitz, Margaret Martonosi, Todd C. Mowry, Michael D. Smith ACM Transactions on C mputer Systems (TOCS), Volume 16 Issue 2



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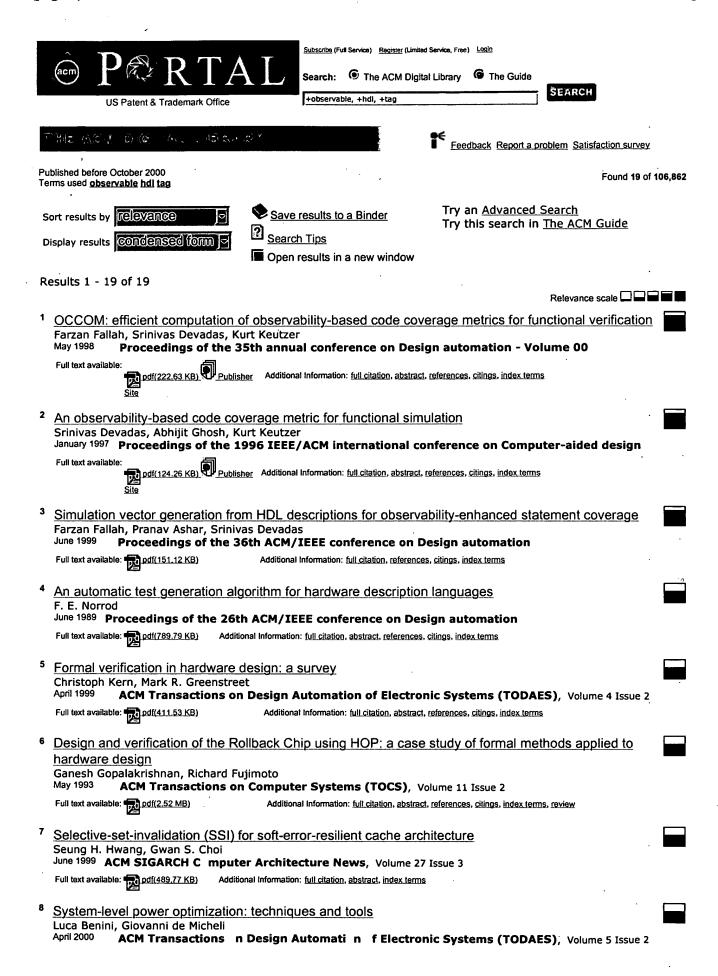
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13		tz, David L. Dill hitecture News , Proceedings of the 22nd annual Computer architecture, Volume 23 Issue 2			
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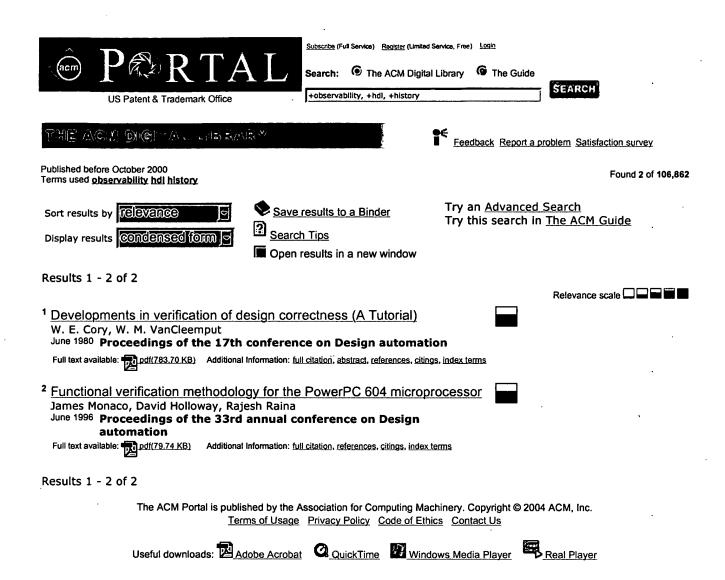
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Costa, J.C.; Devadas, S.; Monteiro, J.C.;

Computer Aided Design, 2000. ICCAD-2000. IEEE/ACM International Conference on , 5-9 Nov. 2000 Pages:27 - 32

[Abstract] [PDF Full-Text (612 KB)] IEEE CNF

2 Simulation vector generation from HDL descriptions for observability-enhanced statement coverage

Fallah, F.; Ashar, P.; Devadas, S.;

Design Automation Conference, 1999. Proceedings. 36th , 21-25 June 1999

Pages:666 - 671

[Abstract] [PDF Full-Text (560 KB)] IEEE CNF

3 OCCOM: efficient computation of observability-based code coverage metrics for functional verification

Fallah, F.; Devadas, S.; Kuetzer, K.;

Design Automation Conference, 1998. Proceedings , 15-19 June 1998

Pages:152 - 157

[Abstract] [PDF Full-Text (692 KB)] IEEE CNF

4 The effect of program behavior on fault observability

Bowen, N.S.; Pradhan, D.K.;

Computers, IEEE Transactions on , Volume: 45 , Issue: 8 , Aug. 1996

Pages:868 - 880

[Abstract] [PDF Full-Text (1224 KB)] IEEE JNL

5 An observability-based code coverage metric for functional simulation

Devadas, S.; Ghosh, A.; Keutzer, K.;

Computer-Aided Design, 1996. ICCAD-96. Digest of Technical Papers., 1996 IEEE/ACM International Conference on , 10-14 Nov. 1996

Pages:418 - 425

[Abstract] [PDF Full-Text (784 KB)] IEEE CNF

6 DC control and observation structures for analog circuits

Yeong-Ruey Shieh; Cheng- Wen Wu;

Test Symposium, 1995., Proceedings of the Fourth Asian , 23-24 Nov. 1995 Pages:120 - 126

[Abstract] [PDF Full-Text (760 KB)] IEEE CNF

7 Program fault tolerance based on mem ry access behavior

Bowen, N.S.; Pradhan, D.K.; Fault-Tolerant Computing, 1991. FTCS-21. Digest of Papers., Twenty-First International Symposium, 25-27 June 1991

Pages:426 - 433

[Abstract] [PDF Full-Text (580 KB)] IEEE CNF

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or structural RTL description. Statements in the HDL description may correspond to hundreds of gates
glenfiddich.lcs.mit.edu/~devadas/pubs/coverage.ps

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Automatic Design Validation Framework for HDL Descriptions... - Liang Zhang And (Correct) we adopted into our framework the **Observability-Based** Code Coverage (OBCC or tag coverage)7] Automatic Design Validation Framework For **Hdl** Descriptions Via Rtl Atpg Liang Zhang And statements, and arithmetic expressions in the **HDL** description. A test environment is a set of www.visc.vt.edu/~mhsiao/papers/ats03lz.pdf

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Coverage Metrics for Temporal Logic Model Checking - Chockler, Kupferman, Vardi (2000) (Correct) (4 citations) [CKV00] H. Chockler, O. Kupferman, and M.Y. Vardi. Coverage metrics for temporal logic www.cs.rice.edu/~vardi/papers/tacas011.ps.gz

RT-level Fault Simulation Techniques based on.. - Corno, Cumani.. (2000) (Correct) metric computes the number of **tags** that reach an **observable** circuit output when the test pattern is higher, thanks to the availability and maturity of **HDL** simulators and synthesis tools. On the other Statement Coverage. They define the concept of **tag** as the possibility that an incorrect value is www.cad.polito.it/pap/db/dcis2000.pdf

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